

ABSTRACT**Process and device for the video coding of high definition images**

The process implements a global regulation at the level of a horizontal band of the image, band consisting of panels, an encoder being assigned to each panel, as a function of a preset bit rate for the band and on the basis of a single Video Buffering Verifier (VBV) taking into account the state of the buffer memories of the encoders of the band.

Fig. 4

Fig. 4 is a block diagram of the process and device for the video coding of high definition images. It shows a sequence of blocks representing the processing steps, including input, encoding, and output stages, connected by arrows indicating the flow of data and control signals.